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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/711,618	09/29/2004	Ling-Wei Ke	MTKP0080USA	5617
27765	7590	01/25/2006	EXAMINER	
NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION			STERRETT, JEFFREY L	
P.O. BOX 506			ART UNIT	
MERRIFIELD, VA 22116			PAPER NUMBER	
			2838	

DATE MAILED: 01/25/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

3/1

Office Action Summary	Application No. 10/711,618	Applicant(s) KE ET AL.	
	Examiner Jeffrey L. Sterrett	Art Unit 2838	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on _____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-24 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 September 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) ☒ All b) ☐ Some * c) ☐ None of:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date 9/29/04.
- 4) ☐ Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.
2. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the bypass capacitor connected to the second receiving terminal of the amplifier circuit as recited by claim 2 must be shown or the feature canceled from the claim. It is noted that in lines 2-4 of claim 1 the first receiving terminal is defined as receiving the reference voltage and the second receiving terminal is defined as receiving the feedback voltage.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next

Office action. The objection to the drawings will not be held in abeyance. No new matter should be entered.

3. Claim 2 is objected to because in line 2 "second" should be –first- (see objection to the drawings above for reasoning). Appropriate correction is required.

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 1, 3-15, and 17-24 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Smith (US 6,414,537).

Smith teaches a voltage regulator circuit as recited by claims 1, 3-15, and 17-24 except for utilizing first and second discharge transistors instead of single discharge transistor 210. The recitation of a series connection of two discharge transistors instead of single discharge transistor 210 across the loading module (capacitor CL and resistors R1 and R2), which is connected between the output node VR/206 and the second voltage source (circuit ground), is not a patentable difference. Whether one of ordinary skill in the art chooses to directly connect, or short-circuit, the output node VR/206 and the second voltage source (circuit ground) by utilizing a single transistor as done by Smith or two transistors as done by applicant is irrelevant since nodes 206 and 208 in Smith are all pulled down to ground by the conduction of transistor 210 just as readily as nodes NOUT and NF1 in applicants circuit are all pulled down to ground by the conduction of transistors 41 and 42.

6. Claims 2 and 16 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Smith.

Smith teaches a voltage regulator circuit as recited by claims 2 and 16 except for utilizing a bypass capacitor on one of the receiving terminals of the amplifier. Official notice is taken that utilizing bypass capacitors to filter or bypass certain frequency components was an old and known expedient in the art at the time of the invention. It would have been obvious to one of ordinary skill in the art at the time of the invention to have modified the voltage regulator circuit of Smith by utilizing a bypass capacitor in order to filter or bypass undesired frequency components as was old and known in the art at the time of the invention.

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

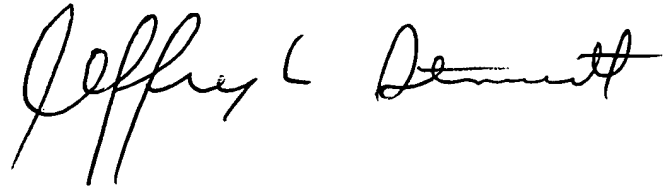
Lee (US 6,177,785) is cited to show a voltage regulator circuit old and known in the art at the time of the invention.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeffrey L. Sterrett whose telephone number is (571) 272-2085. The examiner can normally be reached on Monday-Thursday & 8:00am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Karl D. Easthom can be reached on (571) 272-1989. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jeffrey L. Sterrett
Primary Examiner
Art Unit 2838

A handwritten signature in black ink, appearing to read "Jeffrey L. Sterrett", written in a cursive style.